In conjunction with the **SAFECOMP 2017 Conference**





http://www.safecomp.org

2nd TIPS Workshop

2nd International workshop on the **TI**ming **P**erformance in **S**afety Engineering

12 September 2017, Trento, Italy

Important Dates

Submission deadline: 17 May, 2017 Notification of acceptance: 31 May, 2017 Final version papers: 12 June, 2017 TIPS'17 Workshop: 12 September, 2017

Web: http://safecomp17.fbk.eu/workshop

Organizers Committee

Laurent Rioux Thales R&T, France

Marc Geilen TU Eindhoven, The Netherlands

Program Committee Chairs

Chokri Mraidha CEA, France

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Program Committee

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De-Jiu Chen KTH - Sweden

Dorina C. Petriu Carleton University - Canada

Yiannis Papadopoulos University of Hull - UK

Emmanuel Grolleau ENSMA - France

(Complete list of PC members on the workshop website)

Submission Guidelines

Papers must describe, in English, original work that has not been published or submitted elsewhere. Each paper will be reviewed by at least 3 PC members. The authors will be notified about acceptance before the SAFECOMP 2017 early registration deadline. We solicit three types of submissions:

- Regular papers (up to 15 pages) describing original and unpublished work within the scope of the workshop.
- Short papers (up to 6 pages) describing less mature results or preliminary results.
- Extended abstract (up to 2 pages) for reports on your research project, an industrial experience or your work in progress.

Workshop proceedings will be provided as complementary book to the SAFECOMP Proceedings. Please keep your paper format according to SPRINGER **LNCS** style guidelines (http://www.springer.com/computer/Incs?SGWID=0-164-6-793341-0) (use Microsoft Word if possible).

All papers should be submitted through Easychair:

(https://easychair.org/conferences/?conf=tips17)

Goals

The purpose of the TIPS Workshop is to provide a forum for both practitioners and researchers to present contributions and share ideas, experiences and solutions to concretely connect or integrate performance design engineering and safety engineering activities. Industrial participants are invited to report on efforts applying timing and performance verification techniques in their context of safety or certification, share their insights, and to provide feedback to research priorities and roadmaps. This workshop will also encourage discussions about issues and opportunities to apply timing performance and safety co-engineering.

The TIPS workshop aims to promote discussions, close interactions, cross fertilization of ideas and synergies across the breadth of the timing performance and safety (and security) research communities, as well as to attract industrials from different domains with a specific interest in timing performance and safety engineering.

Topics

Topics include but are not limited to:

- Comparative evaluation of existing integrated timing performance and safety assessment or verification techniques.
- Design modelling languages, methods, techniques and processes for timing performance and safety engineering and combining them.
- New ways for timing performance certification.
- Design architecture evaluation approaches considering timing and safety concerns, including architecture optimizations.
- Performance patterns or solutions for efficient safety and security design.
- Performance predictability for multi-core platforms under safety concerns.
- Virtualization techniques for safe and efficient resource
- Case studies and industrial experience on integrating, combining timing performance and safety engineering activities and certification.
- Discussions on issues and opportunities to integrate, combine timing performance and safety engineering, certifications standards, rules and authorities.
- Demonstrations of techniques or tools to apply timing performance in safety or certification processes.

Reports on European or national research projects (as part of the required dissemination) as well as industrial experience reports from work in progress are welcome.

Contacts:

(workshop and program committee chairpersons)

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